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Amendments to the Specification

Please amend the paragraphs at page 1, lines 4-9, in the following manner:

BACKGROUND1. Technical Field

The disclosed system and method This disclosure relates to a facsimile method for monitoring performance of an image forming apparatus, and more particularly to a facsimile an image forming apparatus which is capable of transmitting measurement data relating to facsimile monitoring its own performance to a control center.

Please amend the paragraph bridging pages 2 and 3, in the following manner:

The present application disclosure provides a facsimile an image forming apparatus which is operatively connected to a central data processing apparatus. In [one]] an exemplary embodiment, the image forming apparatus is configured as a facsimile apparatus and includes a performance measuring mechanism, a memory, a data reading mechanism, and a data transfer mechanism. The performance measuring mechanism measures at least one performance related to facsimile operations. The memory that stores data of measurements on the at least one performance measured by the performance measuring mechanism. The data reading mechanism that receives a request for data transmission and that, in response to the request, reads the data of measurements on the at least one performance stored in the memory. The data transfer mechanism that transfers the data read by the data reading mechanism to the central data processing apparatus.

Please amend the paragraphs at page 3, line 20 through page 4, line 9, in the following manner:

The present application disclosure also provides a facsimile performance

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monitoring system. In [[one]] an exemplary embodiment, the system includes a central data processing apparatus and a plurality of facsimile apparatus. ~~In one embodiment, each facsimile apparatus is operatively connected to the central data processing apparatus.~~ In one embodiment, [[the]] and each facsimile apparatus includes a performance measuring mechanism, a memory, a data reading mechanism, and a data transfer mechanism. The performance measuring mechanism measures at least one performance related to facsimile operations. The memory that stores data of measurements on the at least one performance measured by the performance measuring mechanism. The data reading mechanism that receives a request for data transmission and that, in response to the request, reads the data of measurements on the at least one performance stored in the memory. The data transfer mechanism that transfers the data read by the data reading mechanism to the central data processing apparatus.

Please amend the paragraphs at page 5, lines 7-9, in the following manner:

Figure 1 is a block diagram of a facsimile performance monitoring system including a plurality of facsimile apparatuses, according to an exemplary embodiment of the present application disclosure;

Please amend the paragraph bridging pages 5 and 6, in the following manner:

DETAILED DESCRIPTION

In describing preferred examples and exemplary embodiments illustrated in of the present disclosure with reference to the drawings, specific terminology is employed for the sake of clarity. However, the invention is disclosure and appended claims are not intended to be limited [[to]] by the specific terminology so selected and it is to be understood that each specific element includes all technical equivalents which operate in a similar manner.

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Please amend the paragraphs at page 12, line 24 through page 13, line 14, in the following manner:

The embodiment of the present application examples and exemplary embodiments described herein may be conveniently implemented using a conventional general purpose digital computer programmed according to the teachings of the present specification, as will be apparent to those skilled in the art. Appropriate software coding can readily be prepared based on the teachings of the present disclosure, as will be apparent to those skilled in the art. The subject matter of present application disclosure may also be implemented by the preparation of application specific integrated circuits or by interconnecting an appropriate network of conventional component circuits, as will be readily apparent to those skilled in the art.

This application disclosure is based on Japanese Patent Application No. JPAP09-278036 filed September 26, 1997, the entire contents of which are herein incorporated herein by reference.

Numerous additional modifications and variations of the present application disclosure are possible in light of the above teachings. It is therefore to be understood that within the scope of the appended claims, the present application may be practiced otherwise than as specifically described herein. For example, elements and/or features of different examples and illustrative embodiments may be combined with each other and/or substituted for each other within the scope of this disclosure and appended claims.